

Figure 1 - Device Block Diagram

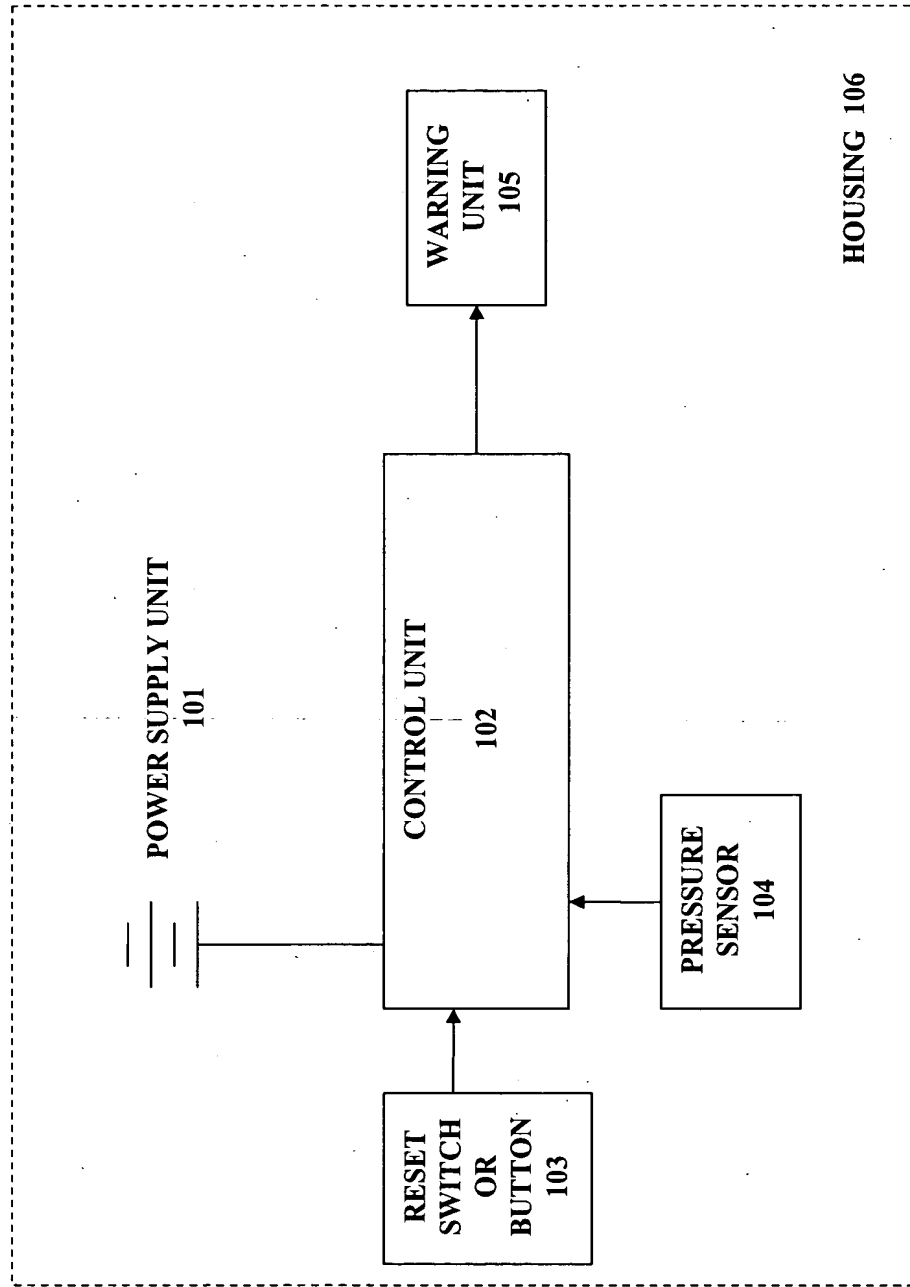


Figure 2 - Device Flow Diagram

